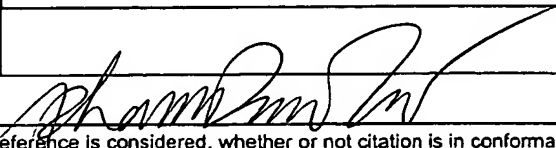


Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY DOCKET NO. <b>248634US2DIV</b>		SERIAL NO. <b>New Applicaiton</b>	
LIST OF REFERENCES CITED BY APPLICANT				APPLICANT <b>Hiroyuki KOBAYASHI, et al.</b>			
				FILING DATE <b>Herewith</b>		GROUP <b>Unassigned</b>	
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
<b>LP</b>	AA	5,708,599 A	01/1998	Sato et al			
	AB	5,524,095 A	06/1996	Someya et al			
	AC	6,177,694 B1	01/2001	El Hajji, Nouredine			
	AD	6,115,296 A	09/2000	Ando, Yasuhiro			
	AE	6,046,627 A	04/2000	Itoh et al			
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	AG						
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	AK						
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	AM						
	AN						
<b>FOREIGN PATENT DOCUMENTS</b>							
	DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO			
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						
	AU						
	AV						
<b>OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
<b>LP</b>	AW	Fariborz ASSADERAGHI, et al. "A Dynamic Threshold Voltage MOSFET (DTMOS) for Very Low voltage Operation", IEEE ELECTRON DEVICE LETTERS, Vol. 15, No. 12, Dec. 1994, pgs 510-512					
<b>LP</b>	AX	In-Young Chung, et al. "A New SOI Inverter Using Dynamic Thershold for Low-Power Applications", IEEE ELECTRON DEVICE LETTERS, Vol. 18, No. 6, June 1997, pgs 248-250					
	AY						
	AZ					<input type="checkbox"/> Additional References sheet(s) attached	
Examiner 				Date Considered <b>06/10/2007</b>			
*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							